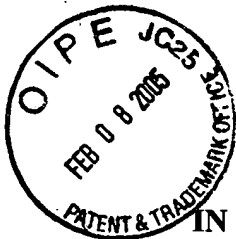


IPD



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2825
Examiner: To be assigned

In Re PATENT APPLICATION OF:

Applicant(s) : Che-Sheng LIN

Serial No. : 10/626,736

Filed : July 25, 2003

For : Circuit design method to control access
pointers of different memory

Attorney Ref: TAIW 154

STATUS REQUEST

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

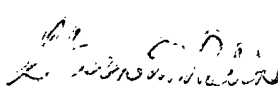
Please let us know the status of the above-identified application and when an
Action may be expected. If the response to this status request is made by telephone, please
ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

February 8, 2005

Date

SMR:dj


Steven M. Rabin
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